

Introduction to Theoretical Computer Science

Lecture 5: Starting on Computability

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Semester 1, 2023/2024

More Pigeonholes

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The same non-terminal V must have appeared as its own descendant in the tree.

Pumping for CFLs

Pumping down Cut the tree at the higher occurrence of V and replace it with the subtree at the lower occurrence of V .

Pumping up Cut at the lower occurrence and replace it with a fresh copy of the higher occurrence.

Pumping Lemma for CFLs

Theorem

If L is context-free then there exists a $p \in \mathbb{N}$ (the pumping length) such that if $w \in L$ with $|w| \geq p$ then w may be split into **five** pieces $w = uvxyz$ such that:

- 1 $uv^i xy^i z \in L$ for all $i \in \mathbb{N}$.
- 2 $|vy| > 0$ and
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- 3 **You** pick a word $w \in L$ with $|w| \geq p$.
- 4 **Adversary** splits it into $uvxyz$ s.t. $|vxy| \leq p$ and $vy \neq \epsilon$.
- 5 **You** win if you can find $i \in \mathbb{N}$ such that $uv^i xy^i z \notin L$. Your prize is a proof of L not being context-free.

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Let $L = \{a^i b^j c^i \mid i > 0\}$. If L is a CFL it must have a pumping length p . Consider the word $w = a^p b^p c^p$. Then, we cannot avoid contradiction no matter how we split $w = uvxyz$:

If vxy is in $a^* b^*$ then uxz (i.e. $uv^0 xy^0 z$) is not in L because **condition 2** says vy contains at least one symbol. So uxz has fewer than p copies of a or b but still p copies of c . Similarly if vxy is in $b^* c^*$.

There are no other cases due to **condition 3**.

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- Otherwise, if vxy spans some part of the first two or last two regions, i.e. a substring of $0^p 1^p$, pumping down will take us out of L .

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- If vxy straddles the midpoint of w , pumping down will remove 1s from the first half but 0s from the second half, taking us out of L .

Chomsky Grammars

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Example

$$\begin{aligned} S &\rightarrow abc \mid aAbc \\ Ab &\rightarrow bA \\ Ac &\rightarrow Bbcc \\ bB &\rightarrow Bb \\ aB &\rightarrow aaA \mid aa \end{aligned}$$

This grammar is called **context-sensitive**

The Chomsky Hierarchy

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- Right-linear is also called...*regular*!

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Given a **regular expression**, we can work *inductively* (see board).

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Emptiness of CFLs

Given a CFG for our language:

- 1 Mark the terminals and ϵ as **generating**.
- 2 Mark as generating all non-terminals which have a production with only generating symbols in their RHS.
- 3 Repeat until nothing new is marked generating.
- 4 Check whether S is marked as generating.

Equivalence

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Equivalence of Regular Languages

Given two DFAs for L_1 and L_2 we can use our standard constructions to produce a DFA of the **symmetric set difference**:

$$(L_1 \cap \overline{L_2}) \cup (L_2 \cap \overline{L_1})$$

(Constructions for complement and intersection are in coursework 1, not lectures.)
If this DFA is empty, then the two languages are equal.

Equivalence Continued

Later we'll develop a theory that allows us to prove rigorously that there are problems that **cannot be solved by any algorithm** that can be implemented as a conventional computer program.

Such problems are called *undecidable*.

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Such problems are called *undecidable*.

Many undecidable problems exist for CFLs:

- Are two CFGs equivalent?
- Is a given CFG ambiguous?
- Is there a way to make a CFG unambiguous?
- Is the intersection of two CFLs empty?
- Does a CFG generate all strings Σ^* ?

Register Machines

Key Insight

There is a general model of computation

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Definition

A *register machine*, or RM, consists of:

- A **fixed** number m of *registers* $R_0 \dots R_{m-1}$, which each hold a natural number.
- A **fixed** *program* P which is a sequence of n *instructions* $l_0 \dots l_{n-1}$

Each instruction is either: $\text{INC}(i)$, which increments register R_i , or $\text{DECJZ}(i, j)$ which decrements R_i unless $R_i = 0$ in which case it jumps to l_j .

Questions of RMs

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Claim

RMs can compute anything any other computer can.

RM ASM

Problem

Programming in RMs directly is very tedious and programs can be overlong.

We will use some simple notation similar to assembly language to simplify it.

Macros

- We'll write them in English, e.g. “add R_i to R_j clearing R_i ”.
- When defining a macro, we'll number instructions from zero, but the instructions are renumbered when macros are expanded. We also use symbolic labels for jumps.
- Macros can use special, negative-indexed registers, guaranteed not to be used by normal programs.

Goto l_j using R_{-1} as temp

0 DECJZ $(-1, j)$

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```
0 DECJZ (-1,j)
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Clear R_i

```
0 DECJZ (i,2)
1 GOTO 0 (using macro above)
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Clear R_i

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Copy R_i to R_j using R_{-2} as temp

```
0 CLEAR Rj
loop1 : 2 DECJZ (i, loop2)
3 INC (j)
4 INC (-2)
5 GOTO loop1
loop2 : 6 DECJZ (-2, end)
7 INC (i)
8 GOTO loop2
end 9
```

RM Programming Exercises

- Addition and subtraction of registers
- Comparison of registers
- Multiplication of registers
- Division/Remainder of Registers

How many registers?

So far, we've just assumed we had as many registers as we needed. But how many do we **actually need**?

Pairing functions

A *pairing function* is an **injective** function $\mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}$.

An example is $f(x, y) = 2^x 3^y$.

We write $\langle x, y \rangle_2$ for $f(x, y)$. If $z = \langle x, y \rangle_2$, let $z_0 = x$ and $z_1 = y$.

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Exercise: Program a pairing function and unpairing functions on a RM.

Exercise: Design (or look up) a surjective pairing function.

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Generalising

Just a 2-tuple pairing function is enough to cram an arbitrary sequence of natural numbers into one $\mathbb{N}^* \rightarrow \mathbb{N}$.

Conclusion

With pairing functions, we can simulate any number of registers using just the registers we need to compute the pairing and unpairing functions, and one user register.

Question

So, how many registers do we **actually need**?