

Lecture 14
**Instruction Selection:
Tree-pattern matching**

(EaC-11.3)

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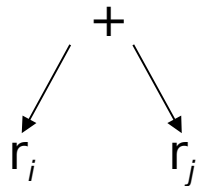
The Concept

Many compilers use tree-structured IRs

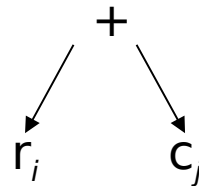
- Abstract syntax trees generated in the parser
- Trees or DAGs for expressions

These systems might well use trees to represent target ISA

Consider the add operators



`add ri, rj ⇒ rk`



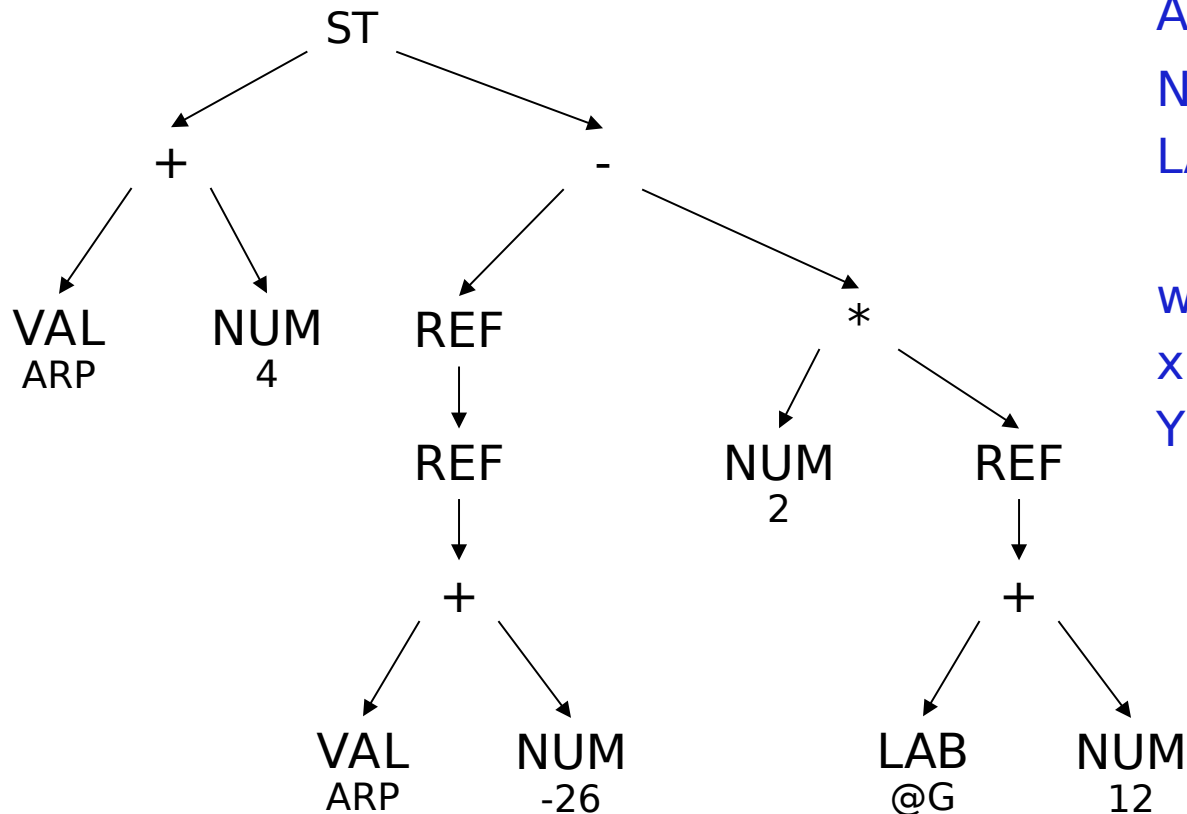
`addI ri, cj ⇒ rk`

} Operation trees

If we can match these “pattern trees” against IR trees, ...

The Concept

Low-level AST for $w \leftarrow (*x) - 2 * y$



ARP: r_{arp}

NUM: constant

LAB: ASM label

w: at ARP+4

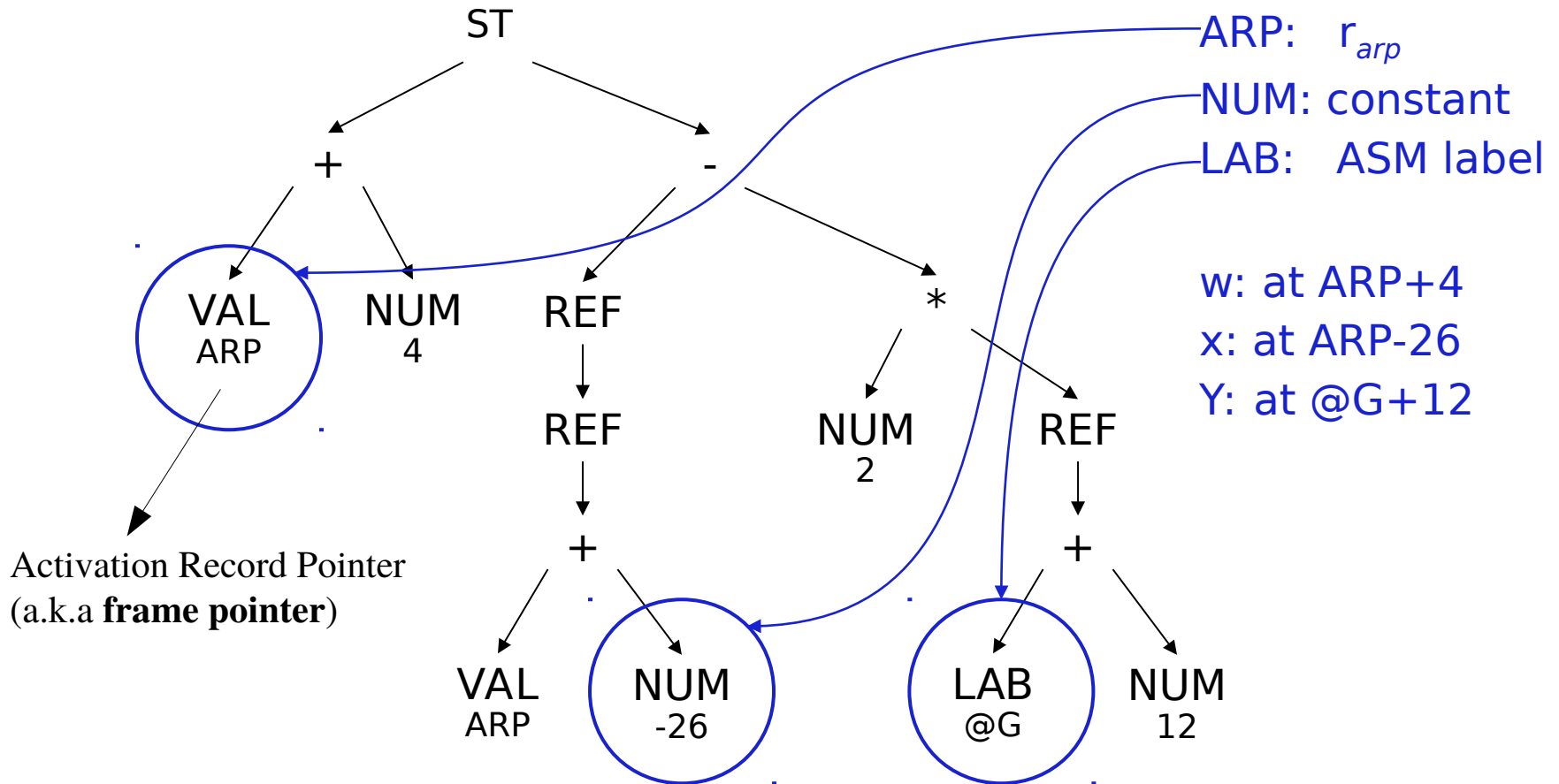
x: at ARP-26

Y: at @G+12

(Ref \approx Load)

The Concept

Low-level AST for $w \leftarrow (*x) - 2 * y$

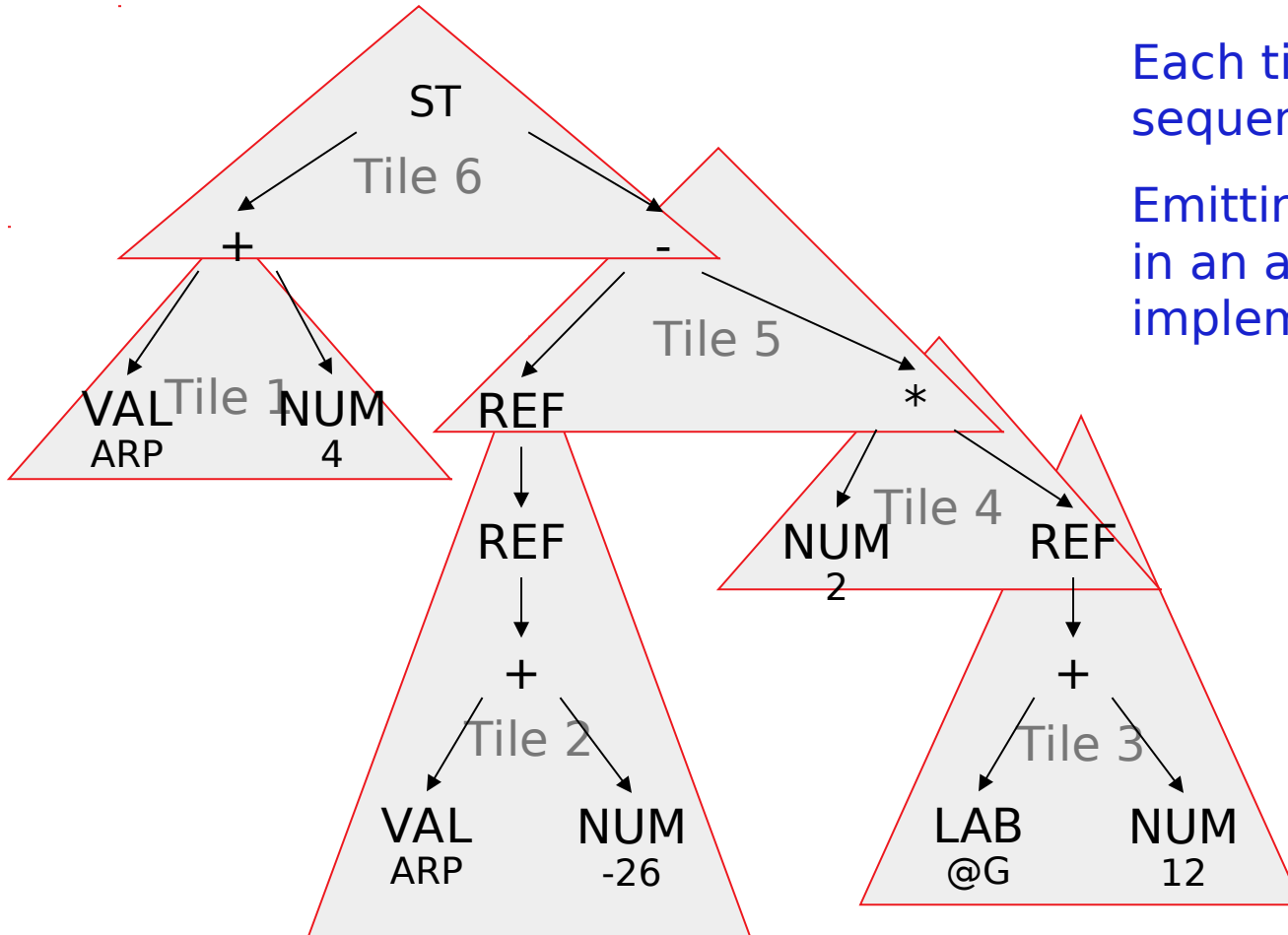


Tree-pattern matching

Goal is to “tile” AST with operation trees

- A tiling is collection of $\langle ast, op \rangle$ pairs
 - ast is a node in the AST
 - op is an operation tree
 - $\langle ast, op \rangle$ means that op could implement the subtree at ast
- A tiling “implements” an AST if it covers every node in the AST and the overlap between any two trees is limited to a single node
 - $\langle ast, op \rangle \in \text{tiling}$ means ast is also covered by a leaf in another operation tree in the tiling, unless it is the root
 - Where two operation trees meet, they must be compatible (expect the value in the same location)

Tiling the Tree



Each tile corresponds to a sequence of operations

Emitting those operations in an appropriate order implements the tree.

Generating Code

Given a tiled tree

- Postorder treewalk, with node-dependent order for children
 - Right child of \leftarrow before its left child
 - Might impose “most demanding first” rule ...
- Emit code sequence for tiles, in order
- Tie boundaries together with register names
 - Tile 6 uses registers produced by tiles 1 & 5
 - Tile 6 emits “`store rtile 5 ⇒ rtile 1`”
 - Can incorporate a “real” allocator or can use “`NextRegister++`”

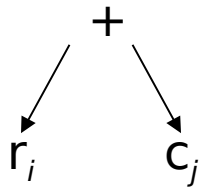
So, What's Hard About This?

Finding the matches to tile the tree

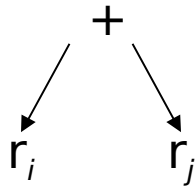
- Compiler writer connects operation trees to AST subtrees
 - Encode tree syntax, in linear form
 - Provides a set of rewrite rules
 - Associated with each is a code template

Notation

To describe these trees, we need a concise notation



$+(r_i, c_j)$



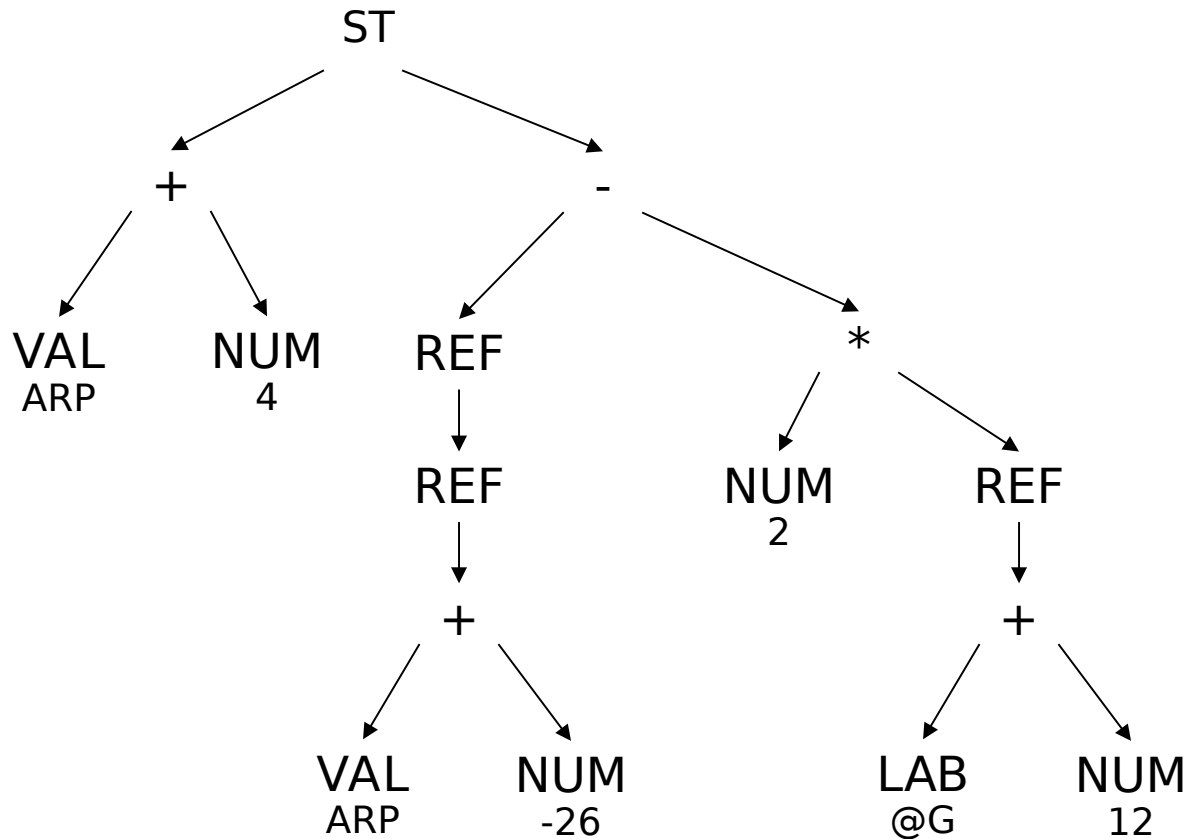
$+(r_i, r_j)$



Linear prefix form

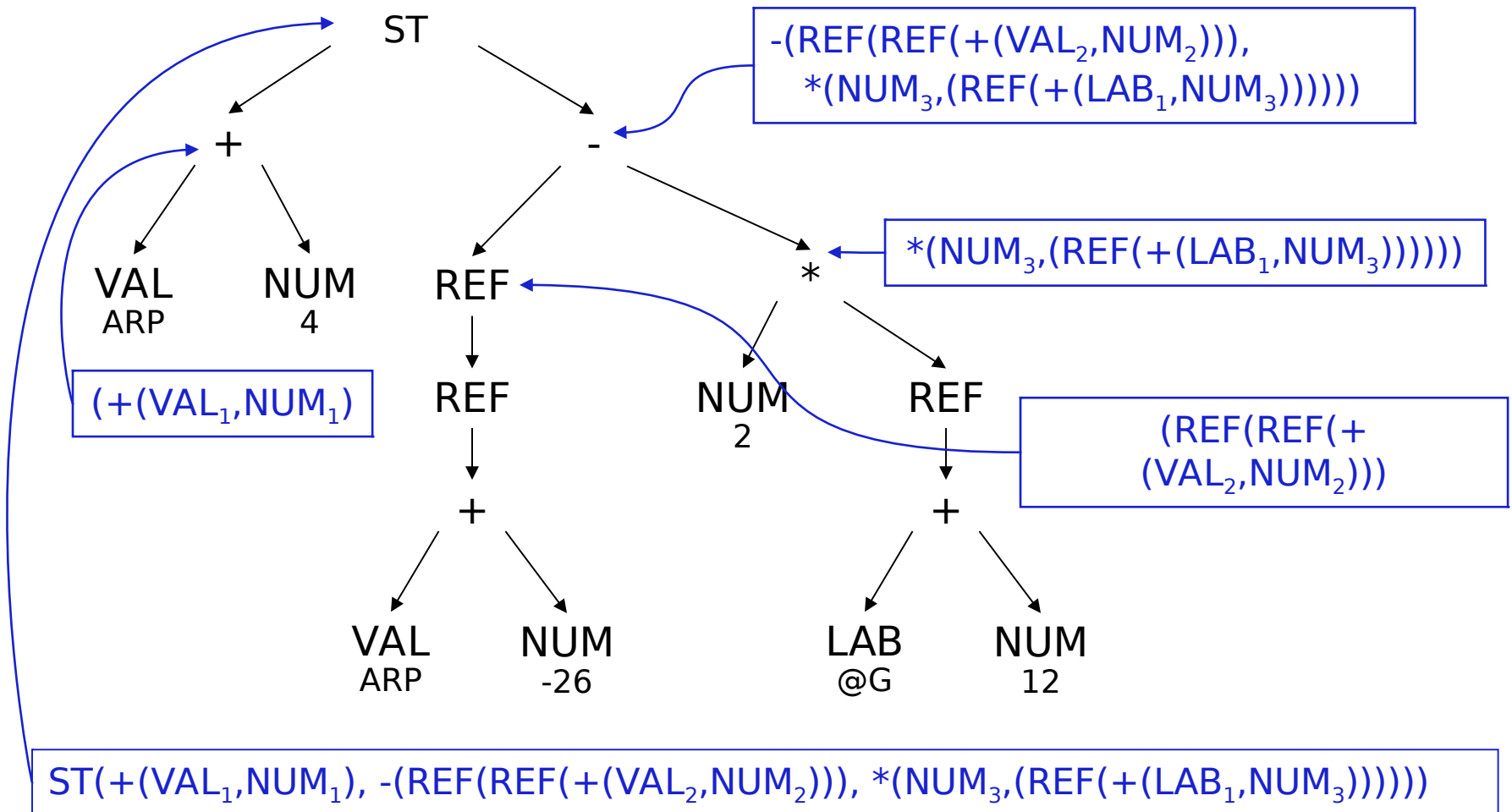
Notation

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Notation

To describe these trees, we need a concise notation



Rewrite rules: LL Integer AST into ILOC

	Rule	Cost	Template
1	Goal \rightarrow Assign	0	
2	Assign \rightarrow ST(Reg ₁ ,Reg ₂)	1	store $r_2 \Rightarrow r_1$
3	Assign \rightarrow ST(+ (Reg ₁ ,Reg ₂),Reg ₃)	1	storeAO $r_3 \Rightarrow r_1, r_2$
4	Assign \rightarrow ST(+ (Reg ₁ ,NUM ₂),Reg ₃)	1	storeAI $r_3 \Rightarrow r_1, n_2$
5	Assign \rightarrow ST(+ (NUM ₁ ,Reg ₂),Reg ₃)	1	storeAI $r_3 \Rightarrow r_2, n_1$
6	Reg \rightarrow LAB ₁	1	loadI $l_1 \Rightarrow r_{new}$
7	Reg \rightarrow VAL ₁	0	
8	Reg \rightarrow NUM ₁	1	loadI $n_1 \Rightarrow r_{new}$
9	Reg \rightarrow REF(Reg ₁)	1	load $r_1 \Rightarrow r_{new}$
10	Reg \rightarrow REF(+ (Reg ₁ ,Reg ₂))	1	loadAO $r_1, r_2 \Rightarrow r_{new}$
11	Reg \rightarrow REF(+ (Reg ₁ ,NUM ₂))	1	loadAI $r_1, n_2 \Rightarrow r_{new}$
12	Reg \rightarrow REF(+ (NUM ₁ ,Reg ₂))	1	loadAI $r_2, n_1 \Rightarrow r_{new}$

Rewrite rules: LL Integer AST into ILOC (*part II*)

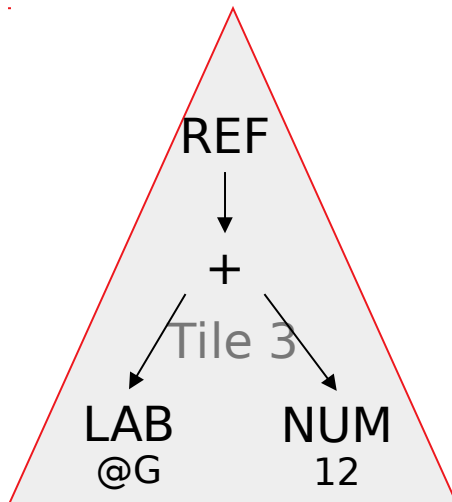
	Rule	Cost	Template
13	$\text{Reg} \rightarrow \text{REF}(+ (\text{Reg}_1, \text{Lab}_2))$	1	loadAI $r_1, l_2 \Rightarrow r_{\text{new}}$
14	$\text{Reg} \rightarrow \text{REF}(+ (\text{Lab}_1, \text{Reg}_2))$	1	loadAI $r_2, l_1 \Rightarrow r_{\text{new}}$
15	$\text{Reg} \rightarrow + (\text{Reg}_1, \text{Reg}_2)$	1	addI $r_1, r_2 \Rightarrow r_{\text{new}}$
16	$\text{Reg} \rightarrow + (\text{Reg}_1, \text{NUM}_2)$	1	addI $r_1, n_2 \Rightarrow r_{\text{new}}$
17	$\text{Reg} \rightarrow + (\text{NUM}_1, \text{Reg}_2)$	1	addI $r_2, n_1 \Rightarrow r_{\text{new}}$
18	$\text{Reg} \rightarrow + (\text{Reg}_1, \text{Lab}_2)$	1	addI $r_1, l_2 \Rightarrow r_{\text{new}}$
19	$\text{Reg} \rightarrow + (\text{Lab}_1, \text{Reg}_2)$	1	addI $r_2, l_1 \Rightarrow r_{\text{new}}$
20	$\text{Reg} \rightarrow - (\text{NUM}_1, \text{Reg}_2)$	1	rsubI $r_2, n_1 \Rightarrow r_{\text{new}}$
...

A real set of rules would cover more than signed integers ...

So, What's Hard About This?

Need an algorithm to AST subtrees with the rules

Consider tile 3 in our example

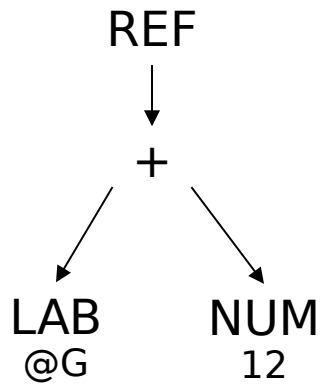


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Consider tile 3 in our example

What rules match tile 3?



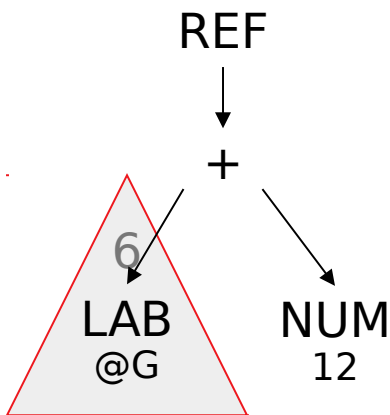
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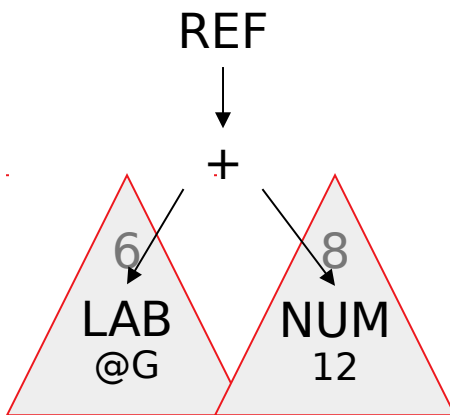
6: $\text{Reg} \rightarrow \text{LAB}_1$ tiles the lower left node



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What rules match tile 3?

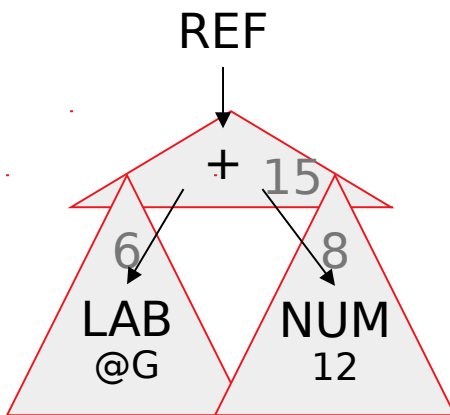
6: $\text{Reg} \rightarrow \text{LAB}_1$ tiles the lower left node

8: $\text{Reg} \rightarrow \text{NUM}_1$ tiles the bottom right node

So, What's Hard About This?

Need an algorithm to AST subtrees with the rules

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What rules match tile 3?

6: $Reg \rightarrow LAB_1$ tiles the lower left node

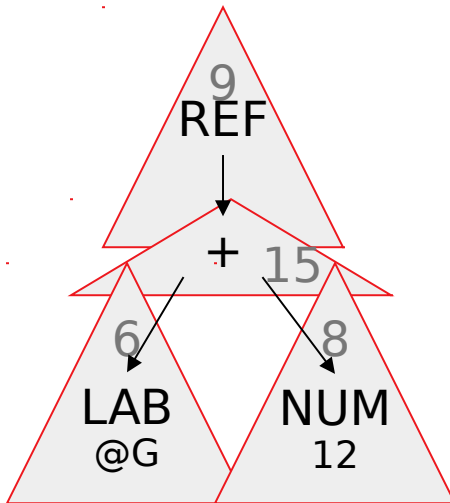
8: $Reg \rightarrow NUM_1$ tiles the bottom right node

15: $Reg \rightarrow + (Reg_1, Reg_2)$ tiles the + node

So, What's Hard About This?

Need an algorithm to AST subtrees with the rules

Consider tile 3 in our example



What rules match tile 3?

6: $\text{Reg} \rightarrow \text{LAB}_1$ tiles the lower left node

8: $\text{Reg} \rightarrow \text{NUM}_1$ tiles the bottom right node

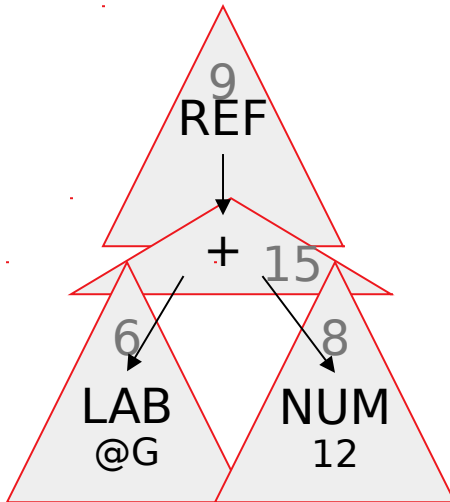
15: $\text{Reg} \rightarrow + (\text{Reg}_1, \text{Reg}_2)$ tiles the + node

9: $\text{Reg} \rightarrow \text{REF}(\text{Reg}_1)$ tiles the REF

So, What's Hard About This?

Need an algorithm to AST subtrees with the rules

Consider tile 3 in our example



What rules match tile 3?

6: $\text{Reg} \rightarrow \text{LAB}_1$ tiles the lower left node

8: $\text{Reg} \rightarrow \text{NUM}_1$ tiles the bottom right node

15: $\text{Reg} \rightarrow + (\text{Reg}_1, \text{Reg}_2)$ tiles the + node

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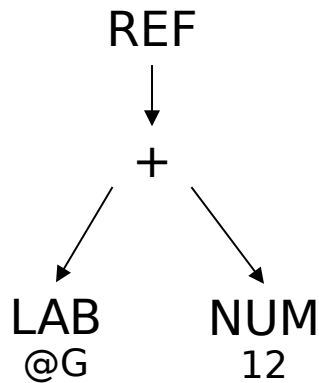
We denote this match as $\langle 6, 8, 15, 9 \rangle$

Of course, it implies $\langle 8, 6, 15, 9 \rangle$

Both have a cost of 4

Finding matches

Many Sequences Match Our Subtree

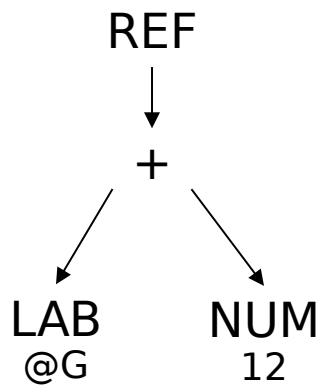


Cost	Sequences			
2	6,11	8,14		
3	6,8,10	8,6,10	6,16,9	8,19,9
4	6,8,15,9	8,6,15,9		

- In general, we want the low cost sequence
- Each unit of cost is an operation (1 cycle)
 - We should favour short sequences

Finding matches

Low Cost Matches



Sequences with Cost of 2	
6: Reg \rightarrow LAB ₁ 11: Reg \rightarrow REF(+ (Reg ₁ , NUM ₂))	loadI @G \Rightarrow r _i loadAI r _i , 12 \Rightarrow r _j
8: Reg \rightarrow NUM ₁ 14: Reg \rightarrow REF(+ (LAB ₁ , Reg ₂))	loadI 12 \Rightarrow r _i loadAI r _i , @G \Rightarrow r _j

These two are equivalent in cost

6,11 might be better, because @G may be longer than the immediate field

Tiling the Tree

Still need an algorithm

- Assume each rule implements one operator
- Assume operator takes 0, 1, or 2 operands

Now, ...

Tiling the Tree

Tile(n)

Label(n) ← ∅

if n has two children then

Tile (left child of n)

Tile (right child of n)

for each rule r that implements n

*if (left(r) ∈ Label(left(n)) and
right(r) ∈ Label(right(n)))*

then Label(n) ← Label(n) ∪ { r }

else if n has one child

Tile(child of n)

for each rule r that implements n

if (left(r) ∈ Label(child(n)))

then Label(n) ← Label(n) ∪ { r }

else / n is a leaf */*

Label(n) ← {all rules that implement n }

Match binary nodes
against binary rules

Match unary nodes
against unary rules

Handle leaves with
lookup in rule table

Notes:

- left and right refer to the children of the AST node or right-hand sides of a rule
- implements: e.g. rule 9 implements REF

Tiling the Tree

Tile(n)

Label(n) ← ∅

if n has two children then

Tile (left child of n)

Tile (right child of n)

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Tile(child of n)

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if (left(r) ∈ Label(child(n))

then Label(n) ← Label(n) ∪ {r}

else / n is a leaf */*

Label(n) ← {all rules that implement n}

This algorithm

- Finds all matches in rule set
- Labels node n with that set
- Can keep lowest cost match at each point
- Leads to a notion of local optimality — lowest cost at each point
- Spends its time in the two matching loops

The Big Picture

- Tree patterns represent AST and ASM
- Can use matching algorithms to find low-cost tiling of AST
- Can turn a tiling into code using templates for matched rules
- Techniques (& tools) exist to do this efficiently

Hand-coded matcher like <i>Tile</i>	Avoids large sparse table Lots of work
Encode matching as an automaton	O(1) cost per node Tools like BURS (bottom-up rewriting system), BURG
Use parsing techniques	Uses known technology Very ambiguous grammars
Linearize tree into string and use string searching algorithm (Aho-Corasick)	Finds all matches

Next Lecture

- Register Allocation