Finite Models





Learning objectives

- Understand goals and implications of finite state abstraction
- Learn how to model program control flow with graphs
- Learn how to model the software system structure with call graphs
- Learn how to model finite state behavior with finite state machines



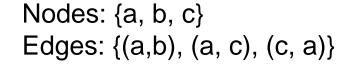
Properties of Models

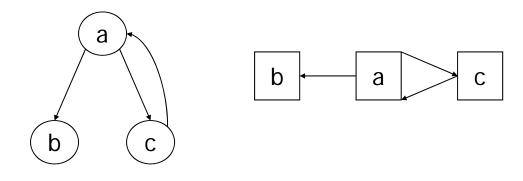
- Compact: representable and manipulable in a reasonably compact form
 - What is *reasonably compact* depends largely on how the model will be used
- **Predictive**: must represent some salient characteristics of the modeled artifact well enough to distinguish between *good* and *bad* outcomes of analysis
 - no single model represents all characteristics well enough to be useful for all kinds of analysis
- Semantically meaningful: it is usually necessary to interpret analysis results in a way that permits diagnosis of the causes of failure
- Sufficiently general: models intended for analysis of some important characteristic must be general enough for practical use in the intended domain of application



Graph Representations: directed graphs

- Directed graph:
 - N (set of nodes)
 - E (relation on the set of nodes) edges

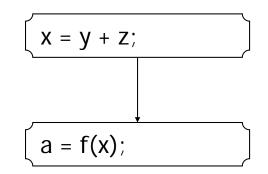


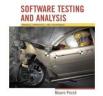




Graph Representations: labels and code

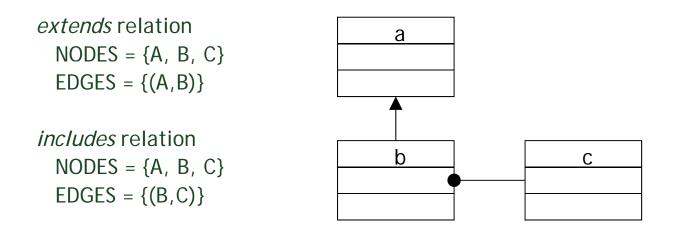
- We can label nodes with the names or descriptions of the entities they represent.
 - If nodes a and b represent program regions containing assignment statements, we might draw the two nodes and an edge (a,b) connecting them in this way:





Multidimensional Graph Representations

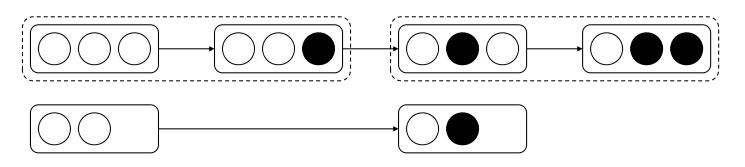
- Sometimes we draw a single diagram to represent more than one directed graph, drawing the shared nodes only once
 - class B extends (is a subclass of) class A
 - class B has a field that is an object of type C





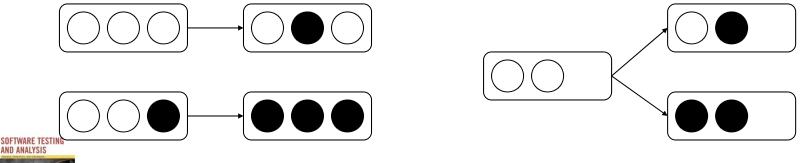
Finite Abstraction of Behavior

an abstraction function suppresses some details of program execution



\Rightarrow

it lumps together execution states that differ with respect to the suppressed details but are otherwise identical





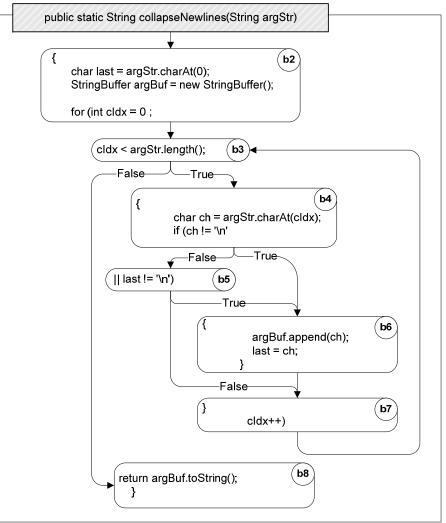
(Intraprocedural) Control Flow Graph

- nodes = regions of source code (basic blocks)
 - Basic block = maximal program region with a single entry and single exit point
 - Often statements are grouped in single regions to get a compact model
 - Sometime single statements are broken into more than one node to model control flow within the statement
- directed edges = possibility that program execution proceeds from the end of one region directly to the beginning of another



Example of Control Flow Graph

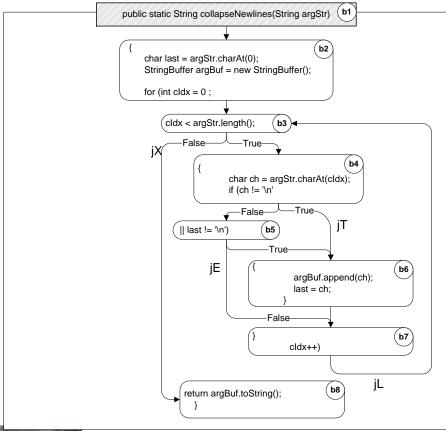
```
public static String collapseNewlines(String argStr)
     char last = argStr.charAt(0);
     StringBuffer argBuf = new StringBuffer();
     for (int cldx = 0; cldx < argStr.length(); cldx++)
        char ch = argStr.charAt(cldx);
       if (ch != '\n' || last != '\n')
          argBuf.append(ch);
          last = ch;
     return argBuf.toString();
```



SOFTWARE TESTING AND ANALYSIS



Linear Code Sequence and Jump (LCSJ) Essentially subpaths of the control flow graph from one branch to another



om Sequence of basic blocs		
intry b1 b2 b3		
b1 b2 b3 b4	jТ	
b1 b2 b3 b4 b5	jЕ	
b1 b2 b3 b4 b5 b6 b7	jL	
b8	ret	
b3 b4	jТ	
b3 b4 b5	jЕ	
b3 b4 b5 b6 b7	jL	
	b1 b2 b3 b1 b2 b3 b4 b1 b2 b3 b4 b5 b1 b2 b3 b4 b5 b6 b7 b8 b3 b4 b3 b4	



Interprocedural control flow graph

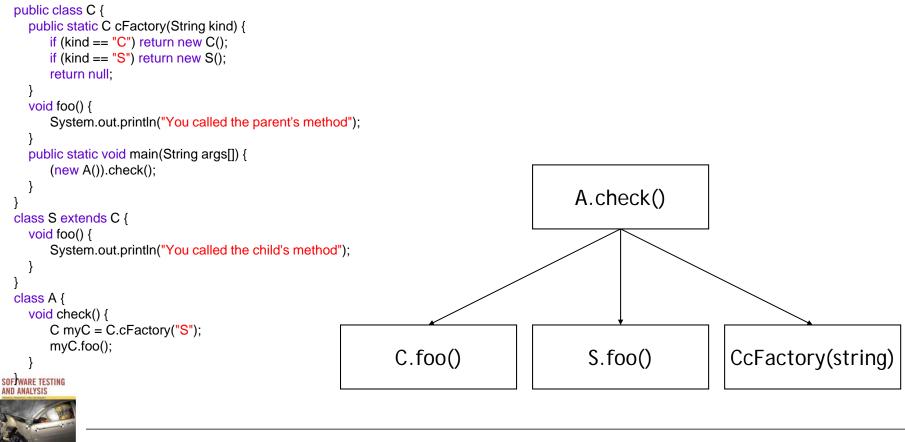
- Call graphs
 - Nodes represent procedures
 - Methods
 - C functions
 - ...
 - Edges represent calls relation



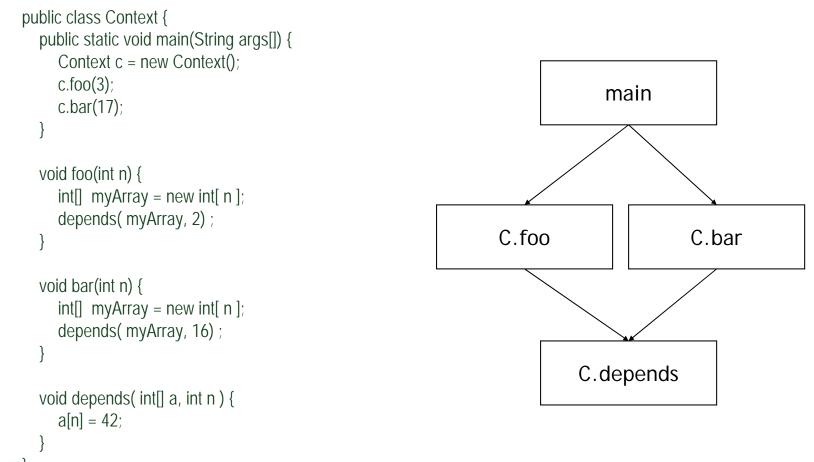


Overestimating the *calls* relation

The static call graph includes calls through dynamic bindings that never occur in execution.



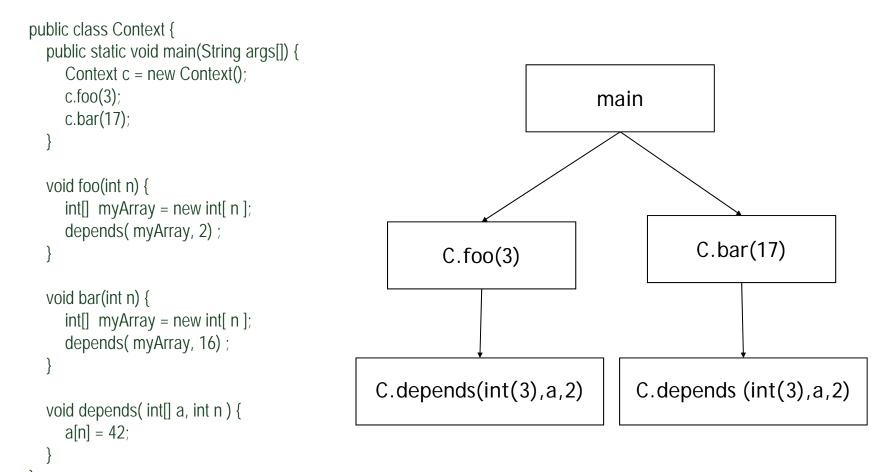
Contex Insensitive Call graphs



SOFTWARE TESTING



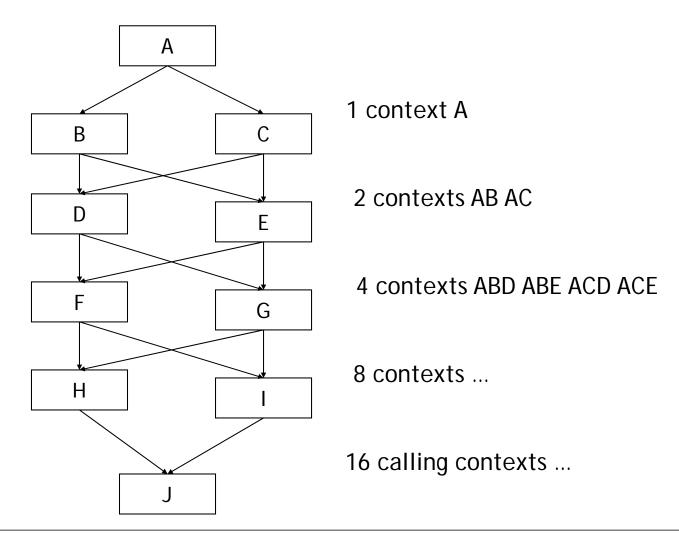
Contex Sensitive Call graphs



SOFTWARE TESTING



Context Sensitive CFG exponential growth

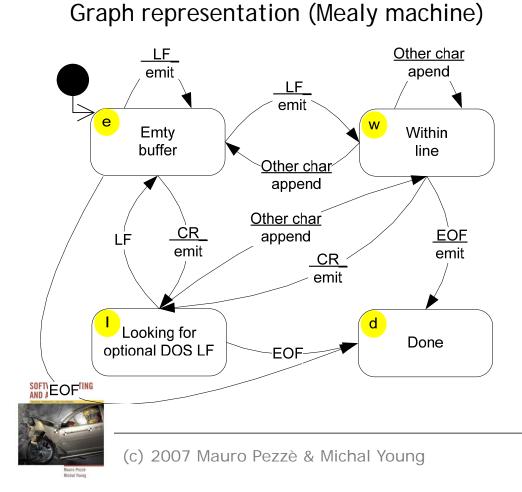


SOFTWARE TESTING



Finite state machines

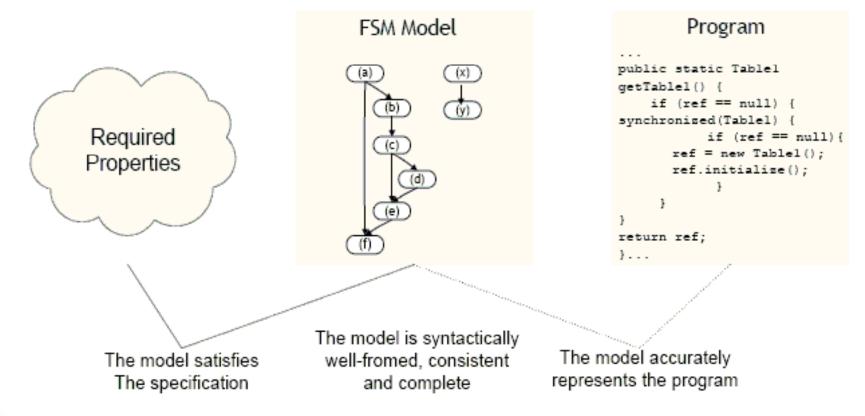
- finite set of states (nodes)
- set of transitions among states (edges)



Tabular representation

	LF	CR	EOF	other
е	e/emit	e/emit	d/-	w/append
w	e/emit	e/emit	d/emit	w/append
I	e/-		d/-	w/append

Using Models to Reason about System Properties







```
/** Convert each line from standard input */
 1
                                                              Abstraction Function
    void transduce() {
 2
з
      #define BUFLEN 1000
4
      char buf[BUFLEN]; /* Accumulate line into this buffer */
5
      int pos = 0;
                      /* Index for next character in buffer */
6
7
      char inChar; /* Next character from input */
8
9
                                                                        Abstract state
                                                                                             Concrete state
      int atCR = 0; /* 0="within line", 1="optional DOS LF" */
10
                                                                                             Lines
                                                                                                        atCR
11
      while ((inChar = getchar()) != EOF ) {
12
                                                                    e (Empty buffer)
                                                                                             3 - 13
                                                                                                        0
        switch (inChar) {
13
                                                                      w (Within line)
                                                                                             13
                                                                                                        0
        case LF:
14
         if (atCR) { /* Optional DOS LF */
15
                                                                  l (Looking for LF)
                                                                                             13
                                                                                                        1
            atCR = 0:
16
                                                                              d (Done)
                                                                                             36
                                                                                                        _
                     /* Encountered CR within line */
17
          } else {
            emit(buf, pos);
18
            pos = 0;
19
20
          break;
21
        case CR:
22
         emit(buf, pos);
23
          pos = 0;
24
          atCR = 1;
25
          break;
26
27
        default:
                                                                           LF
                                                                                         CR
                                                                                                     EOF
         if (pos >= BUFLEN-2) fail("Buffer overflow");
28
                                                                                                     d/-
                                                                           e / emit
                                                                                        1/emit
         buf[pos++] = inChar;
                                                                      е
29
        } /* switch */
30
                                                                                                     d/emit
                                                                           e / emit
                                                                                        1/emit
                                                                      W
31
                                                                                                     d/-
                                                                           e/-
                                                                                        1/emit
      if (pos > 0) {
32
       emit(buf, pos);
33
34
35
```



pos

> 0

other

w / append

w / append

w / append

0

0

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Summary

- Models must be much simpler than the artifact they describe to be understandable and analyzable
- Must also be sufficiently detailed to be useful
- CFG are built from software
- FSM can be built before software to documentintended behavior

