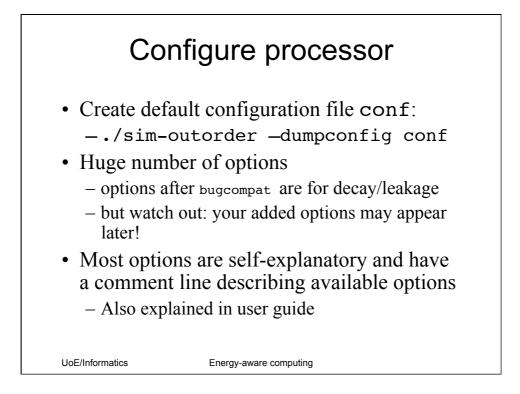


- Download annotated/refactored (slightly) version from http://www.inf.ed.ac.uk/teaching/ courses/eac/hl.tar.gz
 - de-compress and untar somewhere suitable
- First time only:
 - -make config-alpha
 - -make symlinks
- After source code changes:

-make

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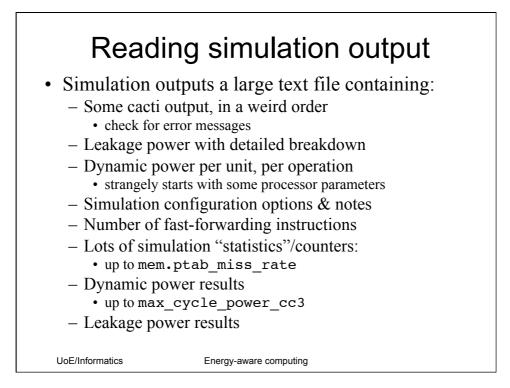
Run a benchmark

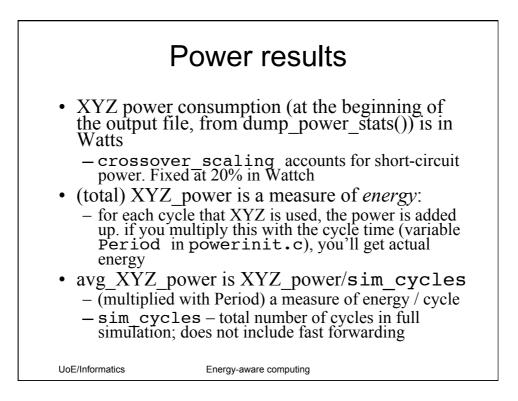
- Download a benchmark from http:// www.inf.ed.ac.uk/teaching/courses/eac/traces/
- Open the conf file and change the:
 - -max:inst 0 to -max:inst 1000000
 - -fastfwd 0 to -fastfwd 10000000
- The above options are just for testing

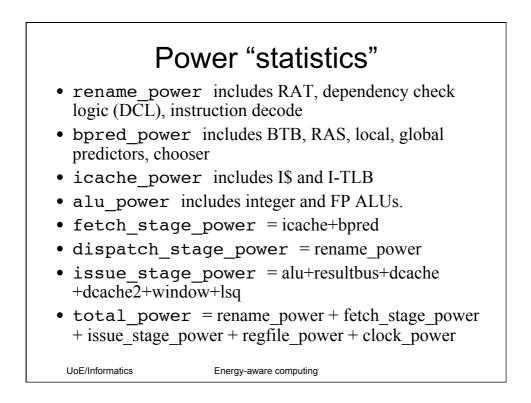
 you should think carefully how to configure, simulate and organize files when experimenting
- ./sim-outorder —config conf <trace> >& trace.out

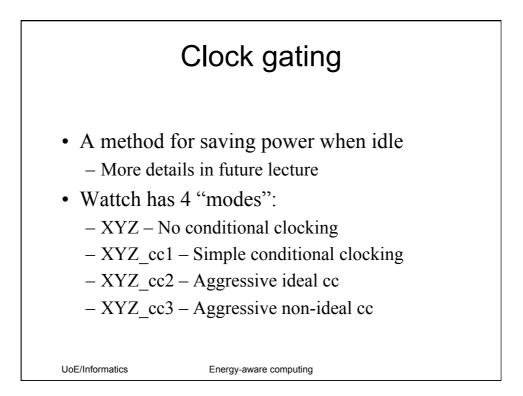
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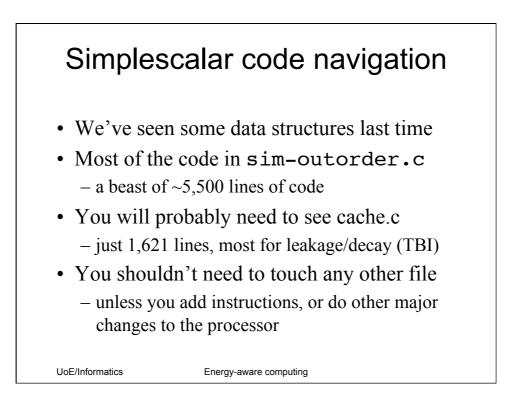
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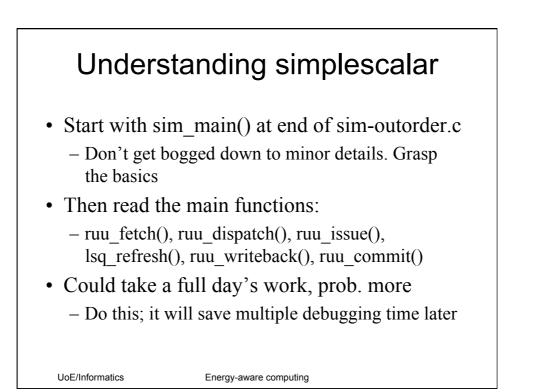












sim_main()	
perform the fast-forward phase	
<pre>perform the fast-forward phase forever do ruu_commit () ruu_release_fu() ruu_writeback() lsq_refresh() ruu_issue() ruu_dispatch() ruu_fetch() • Every iteration is a <i>single cycle</i> • Multiple instructions are handled inside most functions in a loop – superscalar machine: many instructions per cycle</pre>	
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