



Code Shape III

Booleans, Relationals, & Control flow

EaC section 7.4 & 7.8

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Boolean & Relational Values

How should the compiler represent them?

- Answer depends on the target machine

Two classic approaches

- Numerical representation
- Positional (implicit) representation

Correct choice depends on both context and ISA

Boolean & Relational Values

Numerical representation

- Assign values to TRUE and FALSE
- Use hardware AND, OR, and NOT operations
- Use comparison to get a boolean from a relational expression

Examples

$x < y$

becomes

$\text{cmp_LT } r_x, r_y \rightarrow r_1$

if ($l < r$)

stmt_1

else

stmt_2

becomes

$\text{cmp_LT } r_l, r_r \rightarrow r_1$

$\text{cbr } r_1 \rightarrow \text{stmt}_1, \text{stmt}_2$



conditional branch

Boolean & Relational Values

What if the ISA uses a condition code?

- Must use a conditional branch to interpret result of compare
- Necessitates branches in the evaluation

Example:

$x < y$ becomes

```
cmp rx, ry → cc1
cbr_LT cc1 → LT, LF
LT: loadl 1 → r2
      br → LE
LF: loadl 0 → r2
LE: ... other stmts ...
```

This “positional representation” is much more complex

Boolean & Relational Values

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LE: ... other stmts ...
```

Condition codes

- **are an architect's hack**
- **allow ISA to avoid some comparisons**
- **complicates code for simple cases**

This “positional representation” is much more complex

Boolean & Relational Values

The last example actually encodes result in the PC
 If result is used to control an operation, this may be enough

Example

```
if (x < y)
  then a ← c + d
  else a ← e + f
```

VARIATIONS ON THE ILOC BRANCH STRUCTURE

Straight Condition Codes

```
      comp   rx,ry⇒CC1
      cbr_LT CC1 →L1,L2
L1: add   rc,rd⇒ra
      br           →LOUT
L2: add   re,rf⇒ra
      br           →LOUT
LOUT: nop
```

Boolean Compares

```
      cmp_LT  rx,ry⇒r1
      cbr     r1 →L1,L2
L1: add   rc,rd⇒ra
      br           →LOUT
L2: add   re,rf⇒ra
      br           →LOUT
LOUT: nop
```

Boolean & Relational Values

Conditional move & predication both simplify this code

Example
if ($x < y$) then $a \leftarrow c + d$ else $a \leftarrow e + f$

OTHER ARCHITECTURAL VARIATIONS				
<i>Conditional Move</i>			<i>Predicated Execution</i>	
comp	$r_x, r_y \Rightarrow CC_1$		cmp_LT	$r_x, r_y \Rightarrow r_1$
add	$r_c, r_d \Rightarrow r_1$		(r_1)?	add $r_c, r_d \Rightarrow r_a$
add	$r_e, r_f \Rightarrow r_2$		($\neg r_1$)?	add $r_e, r_f \Rightarrow r_a$
i2i_<	$CC_1, r_1, r_2 \Rightarrow r_a$			

Both versions avoid the branches

Both are shorter than CCs or Boolean-valued compare

Are they better?

Boolean & Relational Values

Consider the assignment $x \leftarrow a < b \wedge c < d$

VARIATIONS ON THE ILOC BRANCH STRUCTURE			
<i>Straight Condition Codes</i>		<i>Boolean Compare</i>	
	comp	$r_a, r_b \Rightarrow CC_1$	cmp_LT $r_a, r_b \Rightarrow r_1$
	cbr_LT	$CC_1 \rightarrow L_1, L_2$	cmp_LT $r_c, r_d \Rightarrow r_2$
L ₁ :	comp	$r_c, r_d \Rightarrow CC_2$	and $r_1, r_2 \Rightarrow r_x$
	cbr_LT	$CC_2 \rightarrow L_3, L_2$	
L ₂ :	loadl	0 $\Rightarrow r_x$	
	br	$\rightarrow L_{OUT}$	
L ₃ :	loadl	1 $\Rightarrow r_x$	
	br	$\rightarrow L_{OUT}$	
L _{OUT} :	nop		

Here, the boolean compare produces much better code

Boolean & Relational Values

Conditional move & predication help here, too

$x \leftarrow a < b \wedge c < d$

OTHER ARCHITECTURAL VARIATIONS	
<i>Conditional Move</i>	<i>Predicated Execution</i>
comp r_a, r_b $\Rightarrow CC_1$	cmp_LT $r_a, r_b \Rightarrow r_1$
i2i_< $CC_1, r_T, r_F \Rightarrow r_1$	cmp_LT $r_c, r_d \Rightarrow r_2$
comp r_c, r_d $\Rightarrow CC_2$	and $r_1, r_2 \Rightarrow r_x$
i2i_< $CC_2, r_T, r_F \Rightarrow r_2$	
and $r_1, r_2 \Rightarrow r_x$	

Conditional move is worse than Boolean compares

Predication is identical to Boolean compares

Context & hardware determine the appropriate choice

Control Flow

If-then-else

- Follow model for evaluating relationals & booleans with branches

Branching versus predication (e.g., IA-64)

- Frequency of execution
 - Uneven distribution \Rightarrow do what it takes to speed common case
- Amount of code in each case
 - Unequal amounts means predication may waste issue slots
- Control flow inside the construct
 - Any branching activity within the case base complicates the predicates and makes branches attractive

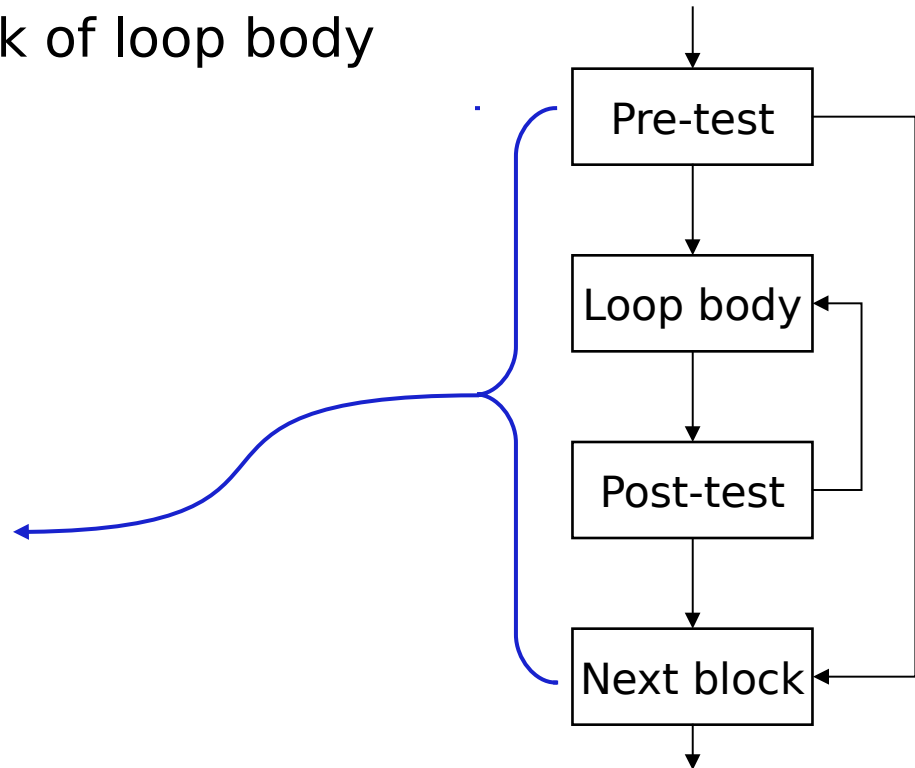
Control Flow

Loops

- Evaluate condition before loop (if needed)
- Evaluate condition after loop
- Branch back to the top (if needed)

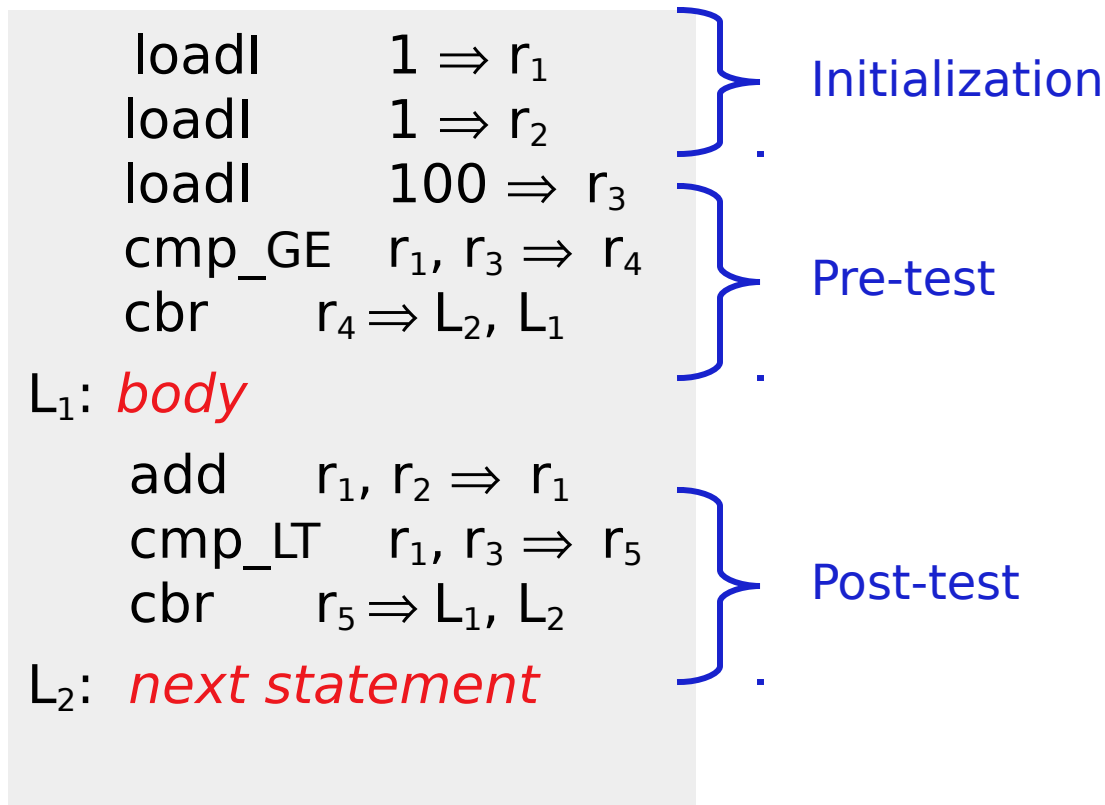
Merges test with last block of loop body

while, for, do, & until all fit this basic model



Loop Implementation Code

```
for (i = 1; i < 100; i++) { body }  
next statement
```



Break statements

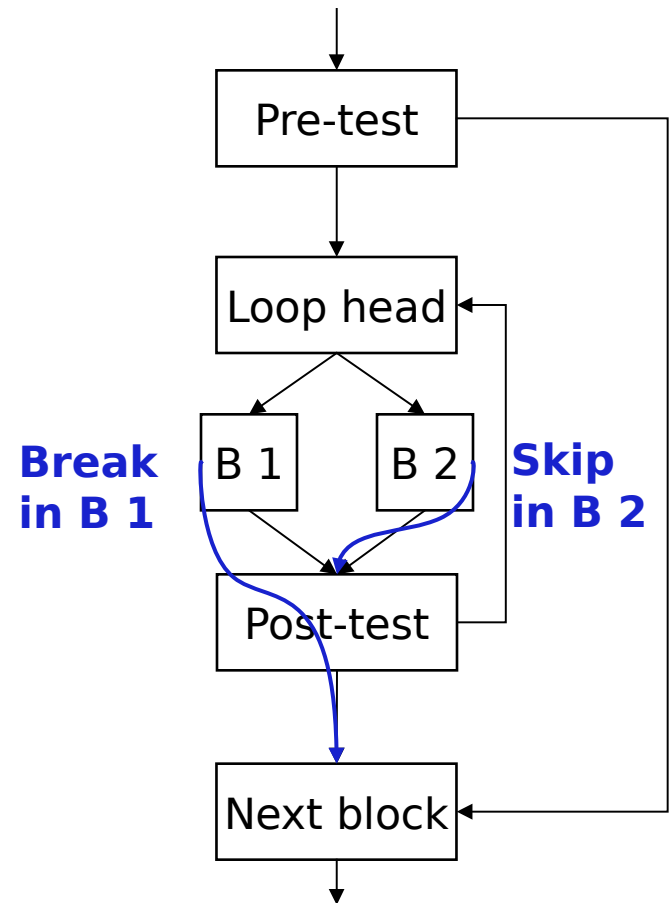
Many modern programming languages include a break

- Exits from the innermost control-flow statement
 - Out of the innermost loop
 - Out of a case statement

Translates into a jump

- Targets statement outside control-flow construct
- Creates multiple-exit construct
- Skip in loop goes to next iteration

Only make sense if loop has > 1 block



Control Flow

Case Statements

- 1 Evaluate the controlling expression
- 2 Branch to the selected case
- 3 Execute the code for that case
- 4 Branch to the statement after the case

Parts 1, 3, & 4 are well understood, part 2 is the key

Control Flow

Case Statements

- 1 Evaluate the controlling expression
- 2 Branch to the selected case
- 3 Execute the code for that case
- 4 Branch to the statement after the case *use break*

Parts 1, 3, & 4 are well understood, part 2 is the key

Strategies

- Linear search (nested if-then-else constructs)
- Build a table of case expressions & binary search it
- Directly compute an address (requires dense case set)



Surprisingly many compilers do this for all cases!